

Graphene: new venues for spintronics

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Spintronics is a paradigm focusing on spin as the information vector in fast and ultra-low-power non volatile devices such as the new STT-MRAM. Beyond its widely distributed application in data storage it aims at providing more complex architectures and a powerful beyond CMOS solution. The recent discovery of graphene has opened novel exciting opportunities in terms of functionalities and performances for spintronics devices. We will present experimental results on the impact and potential of graphene for spintronics. We will show that unprecedented highly efficient spin information transport can occur in graphene [1] leading to large spin signals and macroscopic spin diffusion lengths (~100 microns), a key enabler for the advent of envisioned beyond-CMOS spin-based logic architectures. Furthermore, we will show that a thin graphene passivation layer can prevent the oxidation of a ferromagnet, enabling its use in novel humid/ambient low-cost processes for spintronics devices, while keeping its highly surface sensitive spin current polarizer/analyzer behavior and adding new enhanced spin filtering property[2]. These different experiments unveil promising uses of graphene for spintronics.

[1] B. Dlubak et al., *Nature Physics* **8**, 557 (2012); P. Seneor, et al., *MRS Bulletin* **37**, 1245 (2012)

[2] B. Dlubak et al., *ACS Nano* **6**, 10930 (2012); R. Weatherup, et al., *ACS Nano* **6**, 9996 (2012)